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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/693,748	10/24/2003	Dennis W. Waggamon	125426-1089	2451

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EXAMINER

HOLLOWAY III, EDWIN C

ART UNIT	PAPER NUMBER
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2612

MAIL DATE	DELIVERY MODE
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09/26/2008

PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No. 10/693,748	Applicant(s) WAGGAMON ET AL.	
	Examiner Edwin C. Holloway, III	Art Unit 2612	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 18 July 2008.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 6 and 7 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 6 and 7 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

EXAMINER'S RESPONSE

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 7-18-08 has been entered. The examiner has considered the new presentation of claims and applicant's arguments in view of the disclosure and the present state of the prior art. And it is the examiner's position that the claims are unpatentable for the reasons set forth in this Office action:

Claim Rejections - 35 USC § 112

2. The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

3. Claims 6-7 are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the enablement requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to enable one skilled in the

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art to which it pertains, or with which it is most nearly connected, to make and/or use the invention. Claims 6 and 7 include processing circuitry storing information "randomly" in an unused discrete memory location or "randomly" replacing information stored in one of the used, discrete memory locations. Page 11 of applicant's original written description discloses processing circuitry automatically randomly stores information in unused location so there is no need for any type of means such as an external manually actuated switch for selecting the precise memory location. This specifies only that a manual external switch is not used, but does not provide an enabling disclosure of what circuitry or processing is used to select the memory location. Only a black box decoder circuitry 56 is shown connected to memory 58 in fig. 2. An address circuit or device is needed to select a memory location and such device would obviously include an address manager, address register, pointer, and/or distributor. Applicant's arguments contend that any such device must be non-random, making unclear how the instant specification enables one skilled in the art to which it pertains, or with which it is most nearly connected, to make and/or use the invention.

Claim Rejections - 35 USC § 103

4. The text of those sections of Title 35, U.S. Code not

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included in this action can be found in a prior Office action.

5. Claims 6-7 are rejected under 35 U.S.C. 103(a) as being unpatentable over Brewer'904 (US 5686904) in combination with Roesgen (US 4800524).

Bruwer'904 discloses a secure self learning system with reference to garage door opening in col. 1 lines 22-23. A radio frequency transmitter designated for a manufacturer (manufacturer master key) and having a unique code (unique serial number 62) and multibit hopping codes (32 bit encoded string 70) is included in fig. 2 and col. 14 line 19 - col. 16 line 20. A receiver with memory 84 and processing circuitry 31,100,102, etc. for learning transmitter information is described in co. 19 line 37 - col. 20 line 36. Learning plural transmitters is disclosed in col. 9 line 65. Brewer discloses storing hopping code parameters in available locations of memory during the learn mode (col. 17 line 26 - col. 18 line 2 and col. 19 line 37 - col. 20 line 35). Available locations are considered to be unused. The selected location can be managed by a variety of schemes including cycling through locations and allowing the user to choose. User input may be random, such as direct input on a keypad or buttons such as car stereo presets. Cycling through memory may also be random. But random is not expressly disclosed in Bruwer. The limitation of randomly

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replacing information if all locations are used need not be given weight because it is claimed in the alternative. Further, replacing information is disclosed in col. 2 lines 2-21 of Bruwer.

Roesgen discloses an analogous art memory with pointer that may be accessed consecutively or in a random manner. See col. 1 lines 35-48.

Regarding claims 6-7, it would have been obvious to one of ordinary skill in the art at the time the invention was made to have included in Brewer '904 randomly storing or replacing information in view Roesgen disclosing accessing such memory in a random order scheme that is suggested by Bruwer disclosing next learning pointer can be managed according to a variety of different schemes.

6. Claims 6-7 are rejected under 35 U.S.C. 103(a) as being unpatentable over Brewer'904 (US 5686904) in combination with Sullivan (US 4484262).

Bruwer'904 discloses a secure self learning system with reference to garage door opening in col. 1 lines 22-23. A radio frequency transmitter designated for a manufacturer (manufacturer master key) and having a unique code (unique serial number 62) and multibit hopping codes (32 bit encoded

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string 70) is included in fig. 2 and col. 14 line 19 - col. 16 line 20. A receiver with memory 84 and processing circuitry 31,100,102, etc. for learning transmitter information is described in co. 19 line 37 - col. 20 line 36. Learning plural transmitters is disclosed in col. 9 line 65. Brewer discloses storing hopping code parameters in available locations of memory during the learn mode (col. 17 line 26 - col. 18 line 2 and col. 19 line 37 - col. 20 line 35). Available locations are considered to be unused. The selected location can be managed by a variety of schemes including cycling through locations and allowing the user to choose. User input may be random, such as direct input on a keypad or buttons such as car stereo presets. Cycling through memory may also be random. But random is not expressly disclosed in Bruwer. The limitation of randomly replacing information if all locations are used need not be given weight because it is claimed in the alternative. Further, replacing information is disclosed in col. 2 lines 2-21 of Bruwer.

Sullivan discloses an analogous art shared memory device that randomly distributes information from a plurality of sources S to a plurality of memory units U. The plural sources and plural memory units correspond to the plural transmitters and plural memory banks of Bruwer. Random number

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generator 426 and distributor 421 may provide the random distribution (fig. 4). See the abstract and col. 5 line 57 - col. 6 line 20.

Regarding claims 6-7, it would have been obvious to one of ordinary skill in the art at the time the invention was made to have included in Brewer '904 randomly storing or replacing information in view Sullivan disclosing random distribution of information from plural sources to plural memory units that is suggested by Bruwer disclosing memory with a plurality of blocks for storing information from a plurality of source transmitters using a next learning pointer that can be managed according to a variety of different schemes.

7. Claims 6-7 are rejected under 35 U.S.C. 103(a) as being unpatentable over Brewer'904 (US 5686904) in combination with Roesgen (US 4800524) or Sullivan (US 4484262) as applied above and further in view of Heitschel '118 (US 4750118) or Heitschel '986 (US00RE37986E).

Heitschel'118 discloses an analogous art GDO receiver with random access memory (RAM) in CPU 44 for storing transmitter codes. If all memory location have been used, storing a new code will erase or overwrite at least one old code. See col. 4 lines 58-68. Heitschel '968 is a reissue of Heitschel '118 and

includes selecting a memory address to be erased if all location are used in claim 24.

If replacing information if the memory is full is required by the claims, then such would have been obvious in view of Heitschel '119 or '986 and suggested by the replacing in Brewer.

Response to Arguments

8. Applicant's arguments filed 7-18-08 have been fully considered but they are not persuasive and/or moot in view of new grounds of rejection.

The argument that Heitschel uses a non-random switching arrangement directing learned codes into pre-selected addresses is not persuasive because the claims are not rejected as anticipated by Heitschel alone, but in combination with other references. Further, this argument is moot in view of the rejections relying on Roesgen or Sullivan to teach random memory accessing.

Applicant argues that the examples of cycling the pointer and user selection in Bruwer require sequential, non-random locations. This argument is not persuasive because Brewer does not disclose that these examples require sequential or consecutive locations. Also, Bruwer is not limited to these examples, but refers to other schemes to manage memory. Further, the argument is moot in view of the rejections relying

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on Roesgen or Sullivan to teach random memory accessing.

The arguments regarding Issa and Soenen are moot in view of the new grounds of rejection.

CONTACT INFORMATION

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Edwin C. Holloway, III whose telephone number is (571) 272-3058. The examiner can normally be reached on M-F from 9:00 to 5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Brian Zimmerman, can be reached on (571) 272-3059.

The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

9/25/2008
(571) 272-3058

/Edwin C. Holloway, III/
Primary Examiner, Art Unit 2612